

FORM PTO-892 (REV. 2-92)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		SERIAL NO. 07/852,517	GROUP/UNIT 2508	ATTACHMENT TO PAPER NUMBER	7
NOTICE OF REFERENCES CITED				APPLICANT(S) YAMAZAKI et al.			
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R	WOLF et al., "SILICON PROCESSING FOR THE VLSI ERA," VOL. 1, LATTICE PRESS, SUNSET BEACH, CALIFORNIA, PP. 19-21						
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EXAMINER		DATE					
m. Saadat		3-5-93					
* A copy of this reference is not being furnished with this office action. (See Manual of Patent Examining Procedure, section 707.05 (a).)							